

**Notice of Allowability**

Application No.

09/545,493

Applicant(s)

PIERRE, ROGIER

Examiner

Nilesh Shah

Art Unit

2195

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 7/21/05.
2. ☒ The allowed claim(s) is/are 24-49 now renumbered as claims 1-26.
3. ☐ The drawings filed on \_\_\_\_\_ are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☒ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☒ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

THOMAS LEE  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100

## EXAMINER'S AMENDMENT

### *Drawings*

1. New corrected drawings in compliance with 37 CFR 1.121(d) are required in this application because current drawings are handwritten. Applicant is advised to employ the services of a competent patent draftsman outside the Office, as the U.S. Patent and Trademark Office no longer prepares new drawings. The corrected drawings are required in reply to the Office action to avoid abandonment of the application. The requirement for corrected drawings will not be held in abeyance.
2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
3. Authorization for this examiner's amendment was given in a telephone interview with Mr. Jason Vick (Reg. No. 45,285) on 8/4/05.
4. The application has been amended as follows:

In the claims:

**Replace claim 27 as follows:**

27. A process according to claim 24, wherein said system is of the non-uniform memory access type (NUMA), and the system comprises a predetermined

number of modules linked to one another, wherein each module comprises one of the plurality of processor groups and storage means, and wherein each of said modules is associated with one of said elementary task queues.

**Replace claim 32 as follows:**

32. A process according to claim 24, further comprising at least one additional phase of searching for a remote elementary queue that is not empty when one of said elementary task queues associated with one of said processor groups is empty of executable tasks selecting in said not empty remote elementary queue a task executable by one of said processors of said processor group associated with the empty elementary queue and transmitting said selected task to said one of said processors for processing so as to globally balance the processing of said tasks in said system.

**Replace claim 42 as follows:**

42. Architecture for a multiprocessor digital data processing system, comprising a given number of processors, for implementing a process for assigning tasks to be processed to said processors, said system having a preemptive operating system and said given number of processors capable of processing said tasks in parallel, said processors being divided, in a preliminary phase, into groups of processors; wherein, an elementary task queue is subsequently created and associated with each of the groups of processors; and

each of said elementary task queues stores a plurality of tasks to be processed in a given order of priority, so that each of the stored plurality of tasks of each of said elementary task queues is associated with one of the processors in the group of processors associated with the elementary task queue.

**Replace claim 44 as follows:**

44. Architecture according to claim 42, further comprising, when one of said elementary task queues associated with one of said processors is empty, means for locating non-empty, remote elementary task queue and an executable task in said non empty elementary task queue, and assigning said executable task to said one of said processors for processing said executable task.

**Replace claim 46 as follows:**

46. Architecture according to claim 42, wherein the operating system of the processing system is of the nonuniform memory access type (NUMA), and comprises modules linked to one another, each module comprising a given number of processors and storage means, each of said modules constituting one of said groups, each of said modules being associated with one of said elementary queues.

**Replace claim 47 as follows:**

47. Architecture according to claim 43, wherein the operating system of the processing system is of the nonuniform memory access type (NUMA), and comprises modules linked to one another, each module comprising a given number of processors and storage means, each of said modules constituting one of said groups, each of said modules being associated with one of said elementary queues.

**Replace claim 48 as follows:**

48. Architecture according to claim 44, wherein the operating system of the processing system is of the nonuniform memory access type (NUMA), and comprises modules linked to one another, each module comprising a given number of processors and storage means, each of said modules constituting one of said groups, each of said modules being associated with one of said elementary queues.

**Replace claim 49 as follows:**

49. Architecture according to claim 45, wherein the operating system of the processing system is of the nonuniform memory access type (NUMA), and comprises modules linked to one another, each module comprising a given number of processors and storage means, each of said modules constituting one of said groups, each of said modules being associated with one of said elementary queues.

***Conclusion***

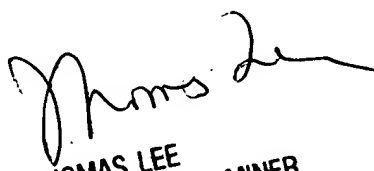
5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nilesh Shah whose telephone number is (571)272-3771. The examiner can normally be reached on 9-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng An can be reached on (571)272-3756. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nilesh Shah  
Examiner  
Art Unit 2195

NS  
August 4, 2005

  
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